



BOSE McKINNEY & EVANS LLP

CUSTOMER NUMBER: 25267

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit: Unknown }
Atty. Docket: 12258-0017 }
Applicant(s): Brodley et al. }
Title: SYSTEM AND METHOD FOR }
PROTECTING FUNCTION }
RETURN ADDRESS }
Serial No.: 10/726,229 }
Filed: December 2, 2003 }
Examiner: Unknown }

Certificate Under 37 C.F.R. 1.8(a)

I hereby certify that this paper or fee is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313

On March 02, 2004

A. Garland

A. Garland

Dated: March 02, 2004

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This statement is filed in the application identified above pursuant to 37 CFR §§ 1.56, 1.97 and 1.98, as amended. Applicants call attention to the art references within the knowledge of applicant and its attorney which are listed on the attached PTO Form 1449. No representation is intended that a complete search has been made or that no better art references are available. The filing of this statement shall not be construed to be an admission that the information cited in the statement is, or is considered to be, material to the patentability as defined in 1.56(b).

Pursuant to the Patent Office Official Gazette Notice dated August 5, 2003, only copies of foreign and non-patent references are submitted.

REMARKS

None of the art cited on the attached PTO Form 1449 is believed to disclose or suggest the invention recited in the claims of the above-identified application. It is therefore believed that the claimed invention is patentably distinguishable over these references.

Please charge any fees that might be due in connection with this Information Disclosure Statement to Bose McKinney & Evans LLP's Deposit Account No. 02-3223. An extra copy of this Information Disclosure Statement is enclosed for that purpose.

Respectfully submitted,

BOSE McKINNEY & EVANS LLP

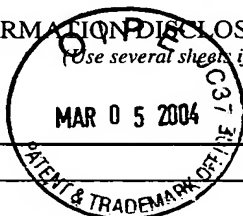


Christine E.M. Orich
Registration No. 44,987

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(317) 684-5000

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FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 12258-0017	SERIAL NO. 10/726,229
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		APPLICANT Brodley et al.	
		FILING DATE December 2, 2003	GROUP Unknown



U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPLICABLE
	AA	5,949,973	09/07/1999	Yarom			
	AB	5,956,481	09/21/1999	Walsh et al.			
	AC	6,092,161	07/18/2000	White et al.			
	AD	6,412,071	06/25/2002	Hollander et al.			
	AE	2001/0013094	08/09/2001	Etoh et al.			
	AF	2002/0007453	01/17/2002	Nemovicher			
	AG	2002/0013907	01/31/2002	May			
	AH	2002/0083343	06/27/2002	Crosbie et al.			
	AI	2002/0144141	10/03/2002	Edwards et al.			
	AJ	2002/0166059	11/07/2002	Rickey et al.			
	AK	2003/0014667	01/16/2003	Kolichtchak			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AR		Stokes, "Understanding the Microprocessor, Part I: Basic Computing Concepts, The Programming Model and the ISA", <i>Arstechnica</i> , June 2002, 4 pages.				
	AS		Stokes, "Understanding the Microprocessor, Part I: Basic Computing Concepts, RAM: when the registers alone don't cut it", <i>Arstechnica</i> , December 2002, 5 pages.				
	AT		Stokes, "Understanding the Microprocessor, Part I: Basic Computing Concepts", <i>Arstechnica</i> , December 2002, 3 pages.				
EXAMINER				DATE CONSIDERED			
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FOREIGN PATENT DOCUMENTS								
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							YES	NO
	BL							
	BM							
	BN							
	BO							
	BP							
	BQ							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)			
	BR		Stokes, "Understanding the Microprocessor, Part I: Basic Computing Concepts, Refining the Model", <i>Arstechnica</i> , December 2002, 4 pages.
	BS		Stokes, "Understanding the Microprocessor, Part I: Basic Computing Concepts, Introduction", <i>Arstechnica</i> , December 2002, 4 pages.
	BT		Stokes, "Understanding the Microprocessor, Part I: Branch Instructions", <i>Arstechnica</i> , December 2002, 4 pages.

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	CA						
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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
	CL						
	CM						
	CN						
	CO						
	CP						
	CQ						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	CR	Aleph One, "Smashing the Stack for Fun and Profit", <i>Phrack Magazine</i> , vol. 7, no. 49, 1997, 23 pages.					
	CS	CERT [®] Coordination Center, "'Code Red' Worm Exploiting Buffer Overflow in IIS Indexing Service DLL", CERT [®] Incident Note IN-2001-08, June 19, 2001, 4 pages, available at www.cert.org/incident_notes/IN-2001-08.html .					
	CT	CERT [®] Coordination Center, "Code Red II: Another Worm Exploiting Buffer Overflow in IIS Indexing Service DLL", CERT [®] Incident Note IN-2001-09, August 6, 2001, 5 pages, available at www.cert.org/incident_notes/IN-2001-09.html .					
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	DA						
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FOREIGN PATENT DOCUMENTS							
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	DL						
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	DQ						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	DR		CERT [®] Advisory CA-2003-20 W32 Blaster Worm", August 11, 2003, 5 pages, available at www.cert.org .				
	DS		Sophos Virus Analysis, August 19, 2003, 3 pages, available at www.sophos.com .				
	DT		CERT [®] Coordination Center, "Buffer Overflow in IIS Indexing Service DLL", CERT [®] Advisory CA-2001-13, June 19, 2001, 4 pages, available at www.cert.org/advisories/CA-2001-13.html .				
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FOREIGN PATENT DOCUMENTS								
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							YES	NO
	EL							
	EM							
	EN							
	EO							
	EP							
	EQ							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)			
ER		CERT® Vulnerability Note VU 568148 Microsoft Windows RPC vulnerable to buffer overflow, 3 pages, available at	
		www.kb.cert.org/vuls/id/56148 .	
ES		CERT® Coordination Center Advisories for 2002, 7 pages, available at www.cert.org/advisories/#2002 .	
ET		SANS Institute, "SANS/FBI Top 20 List: The Twenty most Critical Internet Security Vulnerabilities", 2002, 41 pages, available at	
		www.sans.org/top20/oct02.php .	

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	FO							
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	FQ							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)		
FR		CERT [®] Coordination Center Advisories for 2003, 6 pages, available at www.cert.org/advisories/#2003 .
FS		SANS Institute, SANS Top 20 List: The Twenty Most Critical Internet Security Vulnerabilities, 2003, 64 pages, available at www.sans.org/top20/ .
FT		Newsham, "Format String Attacks", <i>Guardent</i> , September 2000, 8 pages.

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	GQ						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	GR		Anonymous, "Once Upon a Free...", <i>Phrack Magazine</i> , August 2001, vol. 11, no. 57, 15 pages.				
	GS		Solar Designer, "JPEG COM Marker Processing Vulnerability in Netscape Browsers", <i>Bugtraq</i> , July 2000, 4 pages, available at www.securityfocus.com/archive/1/7158 .				
	GT		blexin, "Basic Integer Overflows", <i>Phrack Magazine</i> , December 2002, vol. 11, no. 60, 13 pages.				
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	HR		Solar Designer, "Linux Kernel Patch from the Openwall Project: Non-executable User Stack", January 2001, 4 pages, available at www.openwall.com/linux/README.html .				
	HS		Bhatkar et al., "Address Obfuscation: an Efficient Approach to Combat a Broad Range of Memory Error Exploits", In 12 th USENIX Security Symposium, Washington, D.C., August 2003, 16 pages, available at http://seclab.cs.sunysb.edu .				
	HT		Bulba et al., "Bypassing StackGuard and StackShield", Phrack Magazine, May 2000, vol. 10, no. 56, 12 pages.				
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							YES NO
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	IO						
	IP						
	IQ						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)			
IR		Vendicator, "Stack Shield: A "stack smashing" technique protection tool for Linux", January 2001, 1 page, available at	
		www.angelfire.com/sk/stackshield/download.html .	
IS		Etoh, "GCC Extension for Protecting Applications from Stack-Smashing Attacks", IBM Research, 2 pages, available at	
		www.tri.ibm.com/projects/security/asp/ .	
IT		The OpenBSD Project, April 2003, 1 page, available at www.openbsd.org .	

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	JL						
	JM						
	JN						
	JO						
	JP						
	JQ						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	JR		Snarskii, "FreeBSD Stack Integrity Patch", 1997, 5 pages, available at ftp://ftp.lucky.net/pub/unix/local/libc-letter .				
	JS		Snarskii, "Libparanoia", April 2000, 2 pages, available at www.lexa.ru/snar/libparanoia .				
	JT		Tsai et al., "Libsafe: Protecting Critical Elements of Stacks", Bell Labs, Lucent Technologies, Murray Hill, NJ 07974 USA, Tech. Rep., August 29, 2001, 16 pages, available at www.research.avayalabs.com/techreport/ALR-2001-019-paper.pdf .				
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	KO						
	KP						
	KQ						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	KR		Torvalds, Reply to non-executable stack patch, August 1998, 1 page, available at http://old.lwn.net/1998/0806/a/linus-noexec.html .				
	KS		GNU Compiler Collection (GCC) Internals, 2 pages, available at http://gcc.gnu.org/onlinedocs/gccint/Trampolines.html .				
	KT		Austin et al., "SCC: The Safe C Compiler", June 1994, 1 page, available at www.cs.wisc.edu/~austin/scc.html .				
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)		
LR		Austin, SimpleScalar, LLC, available at www.simplescalar.com .
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